Title: Mixing Signal-Path Setting Apparatus and Program
Inventor(s): Kotaro TERADA and Kei NAKAYAMA
Application No.: Not yet assigned
Docket No.: 393032039800



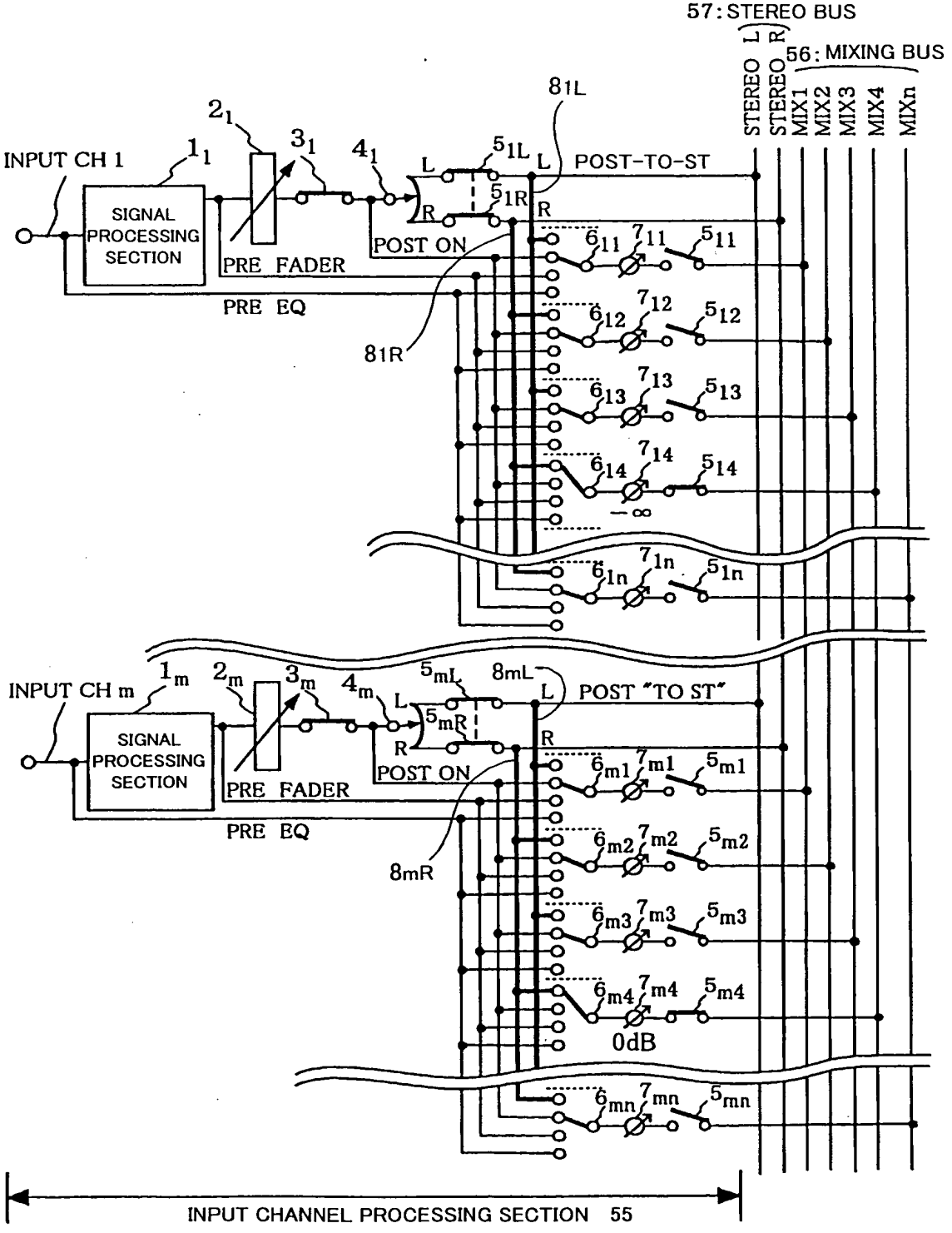



FIG. 1

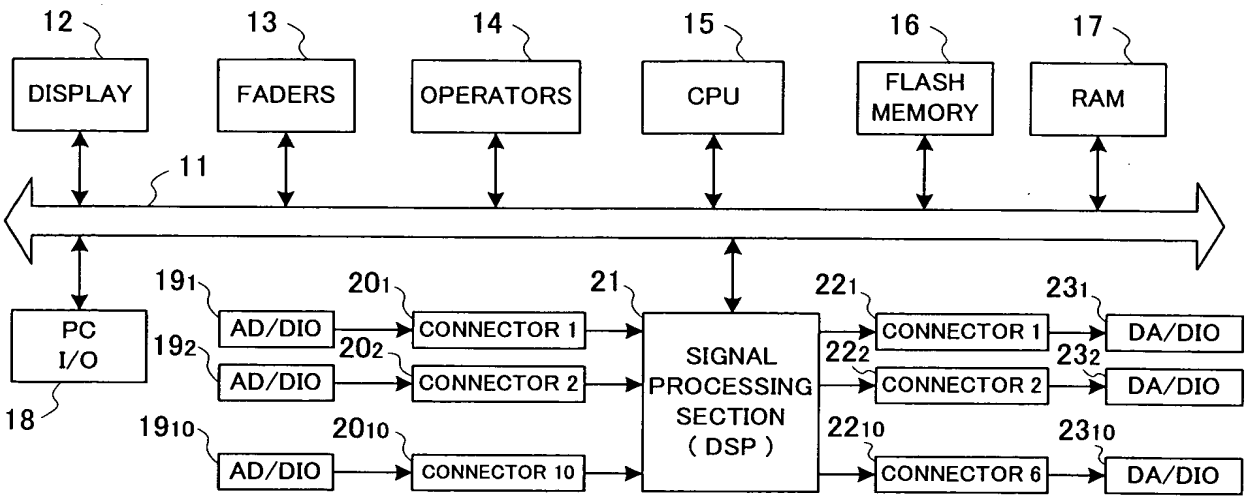

12
13
14
15
16
17
DISPLAY
FADERS
OPERATORS
CPU
FLASH MEMORY
RAM
11
PC I/O
18
19₁
19₂
19₁₀
AD/DIO
AD/DIO
AD/DIO
20₁
20₂
20₁₀
CONNECTOR 1
CONNECTOR 2
CONNECTOR 10
21
SIGNAL PROCESSING SECTION ( DSP )
22₁
22₂
22₁₀
CONNECTOR 1
CONNECTOR 2
CONNECTOR 6
23₁
23₂
23₁₀
DA/DIO
DA/DIO
DA/DIO

FIG. 2

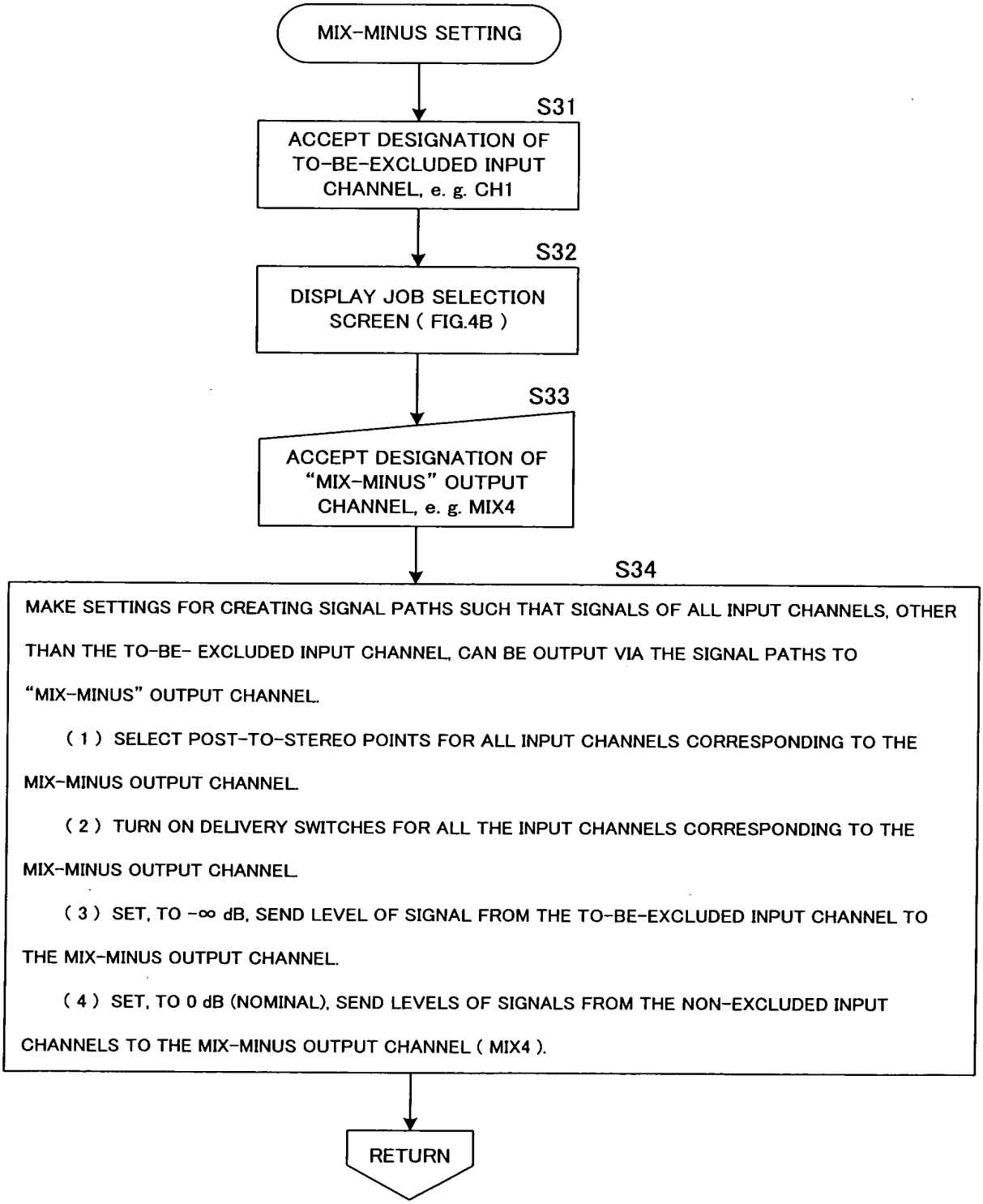

MIX-MINUS SETTING
S31
ACCEPT DESIGNATION OF TO-BE-EXCLUDED INPUT CHANNEL, e. g. CH1
S32
DISPLAY JOB SELECTION SCREEN ( FIG.4B )
S33
ACCEPT DESIGNATION OF "MIX-MINUS" OUTPUT CHANNEL, e. g. MIX4
S34
MAKE SETTINGS FOR CREATING SIGNAL PATHS SUCH THAT SIGNALS OF ALL INPUT CHANNELS, OTHER THAN THE TO-BE- EXCLUDED INPUT CHANNEL, CAN BE OUTPUT VIA THE SIGNAL PATHS TO "MIX-MINUS" OUTPUT CHANNEL.
( 1 ) SELECT POST-TO-STEREO POINTS FOR ALL INPUT CHANNELS CORRESPONDING TO THE MIX-MINUS OUTPUT CHANNEL.
( 2 ) TURN ON DELIVERY SWITCHES FOR ALL THE INPUT CHANNELS CORRESPONDING TO THE MIX-MINUS OUTPUT CHANNEL.
( 3 ) SET, TO -∞ dB, SEND LEVEL OF SIGNAL FROM THE TO-BE-EXCLUDED INPUT CHANNEL TO THE MIX-MINUS OUTPUT CHANNEL.
( 4 ) SET, TO 0 dB (NOMINAL), SEND LEVELS OF SIGNALS FROM THE NON-EXCLUDED INPUT CHANNELS TO THE MIX-MINUS OUTPUT CHANNEL ( MIX4 ).
RETURN

FIG. 3

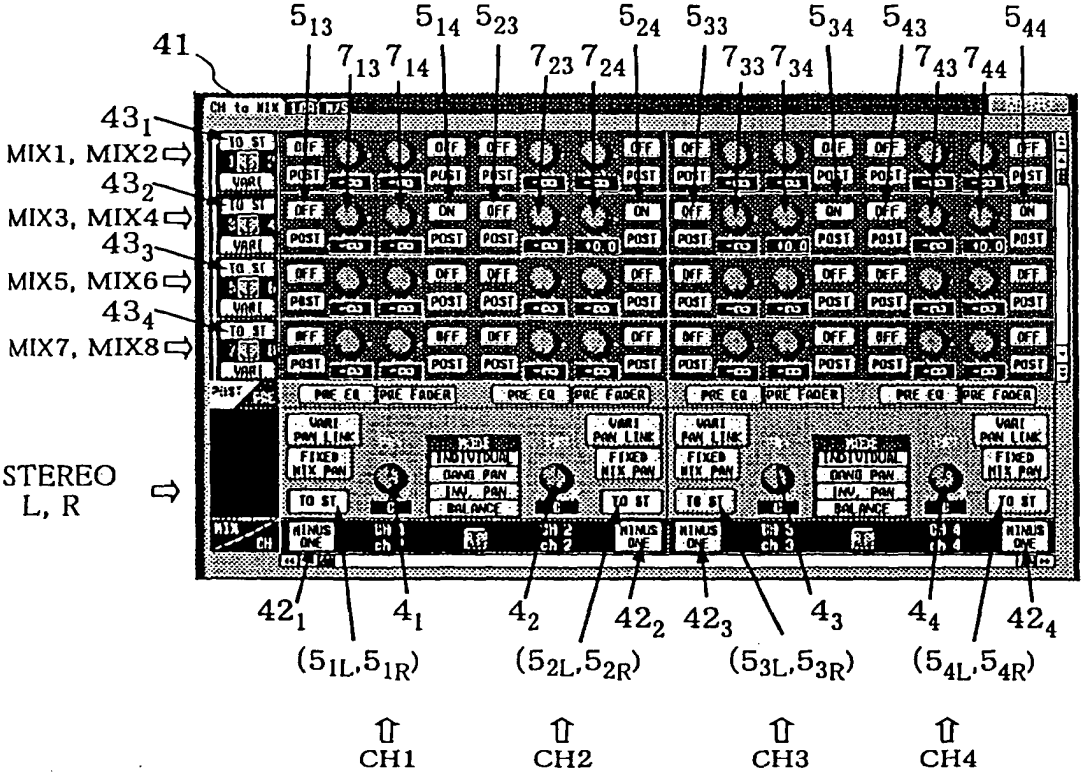

41
$5_{13}$ $5_{14}$ $5_{23}$ $5_{24}$ $5_{33}$ $5_{34}$ $5_{43}$ $5_{44}$
$7_{13}$ $7_{14}$ $7_{23}$ $7_{24}$ $7_{33}$ $7_{34}$ $7_{43}$ $7_{44}$
$43_1$
MIX1, MIX2
$43_2$
MIX3, MIX4
$43_3$
MIX5, MIX6
$43_4$
MIX7, MIX8
STEREO
L, R
$42_1$ $4_1$ $4_2$ $42_2$ $42_3$ $4_3$ $4_4$ $42_4$
$(5_{1L},5_{1R})$ $(5_{2L},5_{2R})$ $(5_{3L},5_{3R})$ $(5_{4L},5_{4R})$
CH1 CH2 CH3 CH4

FIG. 4A

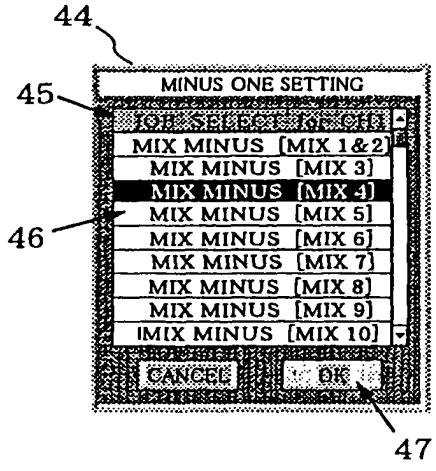
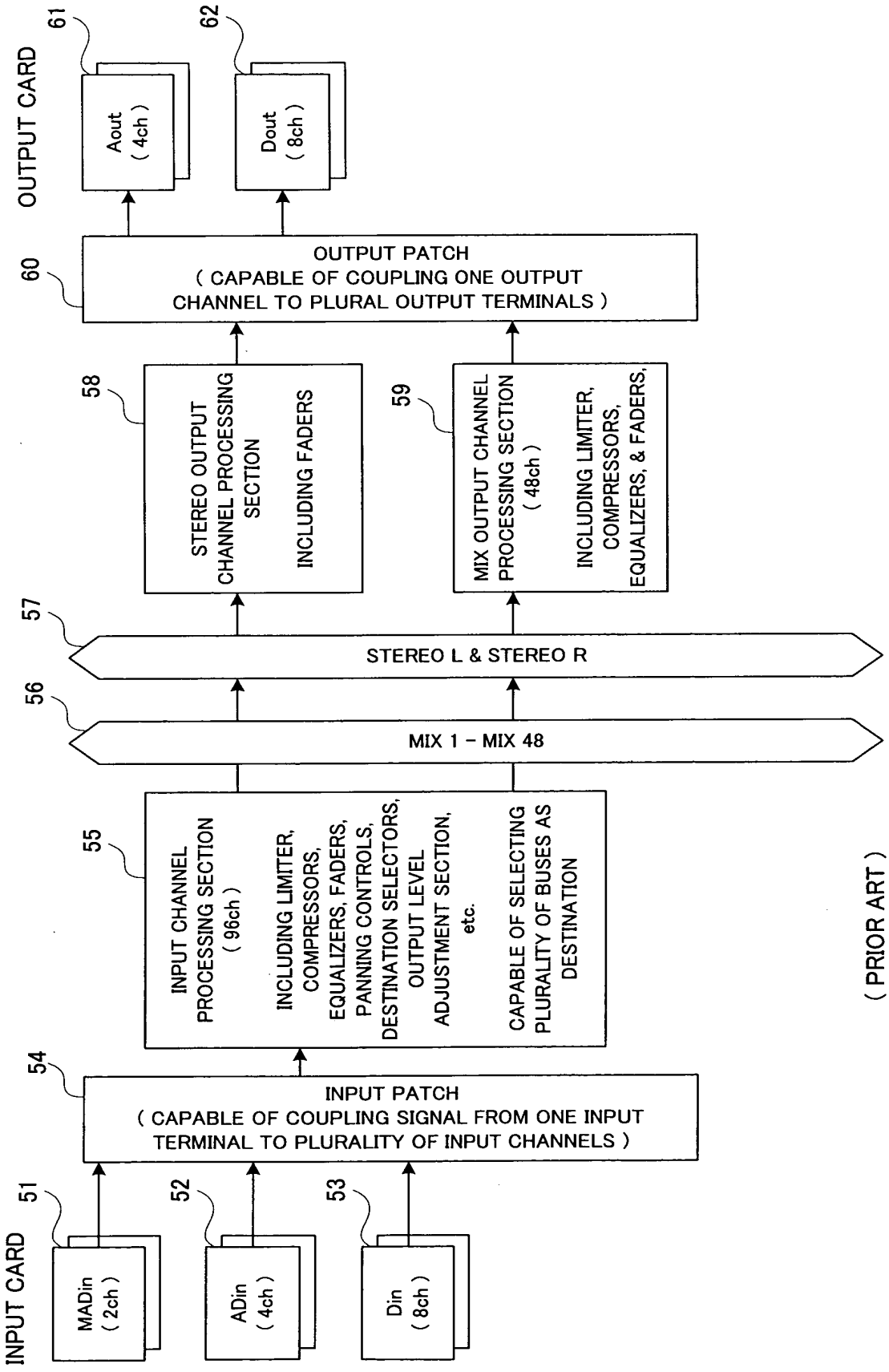

44
45
46
47
MINUS ONE SETTING
TOP SELECT for CH1
MIX MINUS [MIX 1&2]
MIX MINUS [MIX 3]
MIX MINUS [MIX 4]
MIX MINUS [MIX 5]
MIX MINUS [MIX 6]
MIX MINUS [MIX 7]
MIX MINUS [MIX 8]
MIX MINUS [MIX 9]
MIX MINUS [MIX 10]
CANCEL
OK

FIG. 4B




( PRIOR ART )

FIG. 5